

File No.: 6446-22US JA/AD(mb

Montreal, Canada
September 22, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chunyan WANG

Serial No.: (unknown)

Filed: September 23, 2003

Title: METHOD AND APPARATUS FOR CURRENT AMPLIFICATION

Examiner: (unknown)

Agent of Record: James Anglehart Tel: Direct Dial: 514) 847-4244

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PRIOR TO FIRST OFFICE ACTION

Sir:

Pursuant to the duty of disclosure under 37 CFR 1.56, copies of the references listed on the attached PTO Form 1449 are submitted herewith.

The Examiner is kindly requested to consider these references during the examination of the above-identified application, making the references of record, and to return an initialed copy of the PTO-1449 Form to the below-signed agent.

In accordance with 37 CFR 1.97(h), the submission of the present information is not to be construed as an admission that such information is, or is considered to be material to patentability.

Respectfully submitted,

Chunyan WANG

By:



C. Marc Benoît

Agent of Record, Registration No. 50,200

OGILVY RENAULT

1600 - 1981 McGill College Avenue

Montreal, Quebec, Canada H3A 2Y3

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		Application Number	(unknown)
		Filing Date	September 23, 2003
		First Named Inventor	Chunyan WANG
		Art Unit	(unknown)
		Examiner Name	(unknown)
Sheet	1	of	2
		Attorney Docket Number	6446-22US JA/AD/mb

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		WIDEBAND CLASS AB (PUSH-PULL) CURRENT AMPLIFIER IN CMOS TECHNOLOGY, Electronics Letters 12th. April 1990, Vol. 26, No. 8, pp.543-545.	
		CONSTANT-BANDWIDTH CURRENT MODE OPERATIONAL AMPLIFIER, E. Bruun, ELECTRONICS Institute, Technical University of Denmark, Building 349, DK-2800 Lyngby, Denmark, 2 nd . July 1991, pages.	
		A CMOS CURRENT-MODE OPERATIONAL AMPLIFIER, Thomas Kaulberg, IEEE Journal of Solid-State Circuits, Vol. 28, No. 7, July 1992, pp. 849-852.	
		A 200 MHz Steered Current Operational Amplifier in 1.2- μ m CMOS Technology, Eyad Abou-Allam and Ezz El-Masry, IEEE Journal of Solid-State Circuits, Vol. 32, No. 2, February 1997, pp. 245-249.	
		High-Drive CMOS Current Amplifier, Giuseppe Palmisano, Gaetano Palumbo, Member IEEE, and Salvatore Pennisi, IEEE Journal of Solid-State Circuits, Vol. 33, No. 2, February 1998, pp. 228-236.	
		Application of the Back Gate in MOS Weak Inversion Translinear Circuits, Jan Mulder et al., IEEE Transactions on Circuits and Systems-I: Fundamental Theory and Applications, Vol. 42, No. 11, November 1995, pp. 958-962.	
		7-Decade Tuning Range CMOS OTA-C. T. Serrano-Gotarredona and B. Linares-Barranco National Microelectronics Center (CNM)' Ed. CICA, IEEE, 1998, Electronics Letters on Line No. 19981200, 2 pages.	

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